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Application Number	10/608588
Filing Date	6/27/2003
First Named Inventor	POLYAKOV, Evgeny
Art Unit	2664
Examiner Name	MAIS, Mark A.
Attorney Docket Number	TERAD-8-US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/M.M./		IEEE 1394 and the Windows Platform: The optimal PC interconnect bus for consumer electronic devices, Dec. 14, 2001.	
/M.M./		ANONYMOUS: "Reference Manual: Model 2108; VXI Serial Data System Digital Resource Module"--Online-, Feb. 8, 2001, Talon Instruments, XP002274335.	
/M.M./		ANONYMOUS: "Application Note #2; Model 2108; 1553 Serial Bus Emulation"--Online-, 2001, Talon Instruments, XP002274336.	
/M.M./		ANONYMOUS: "Reference Manual: Model 2108; TX01/RX01 Interconnect Modules"--Online-, Jan. 24, 2001, Talon Instruments, XP002274337.	

Examiner Signature	/Mark Mais/	Date Considered	7/18/2008
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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